

Perp. to Fin view

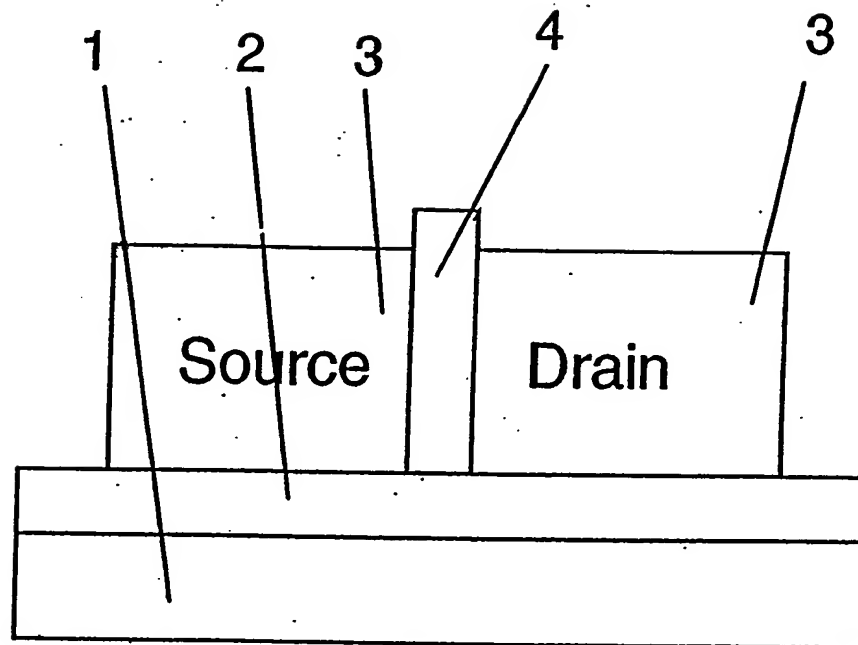


Fig 1
Prior Art

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Parallel to Fin view

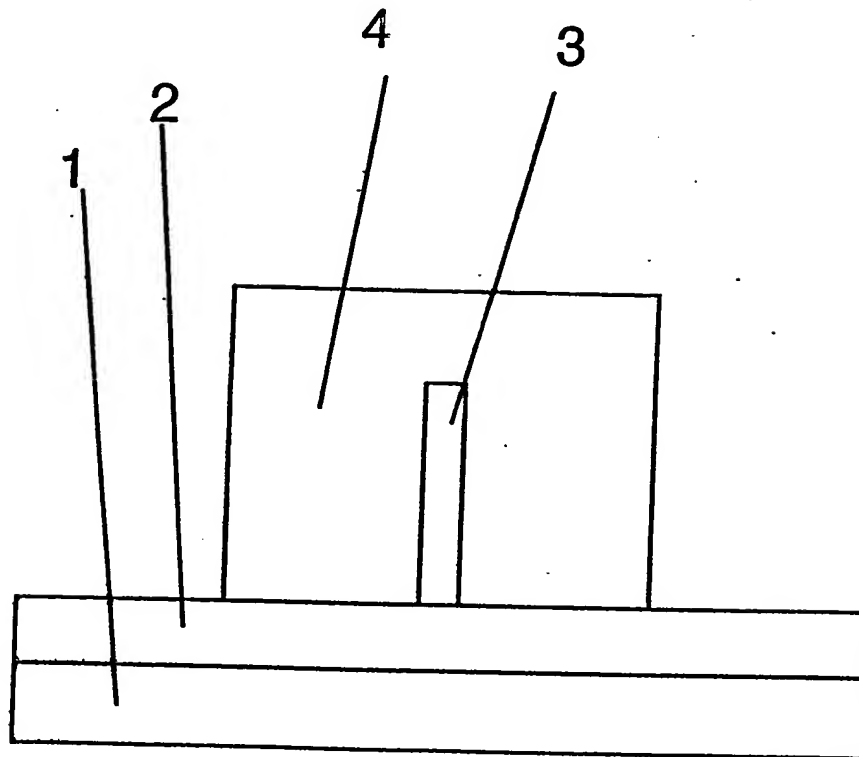


Fig 2
Prior Art

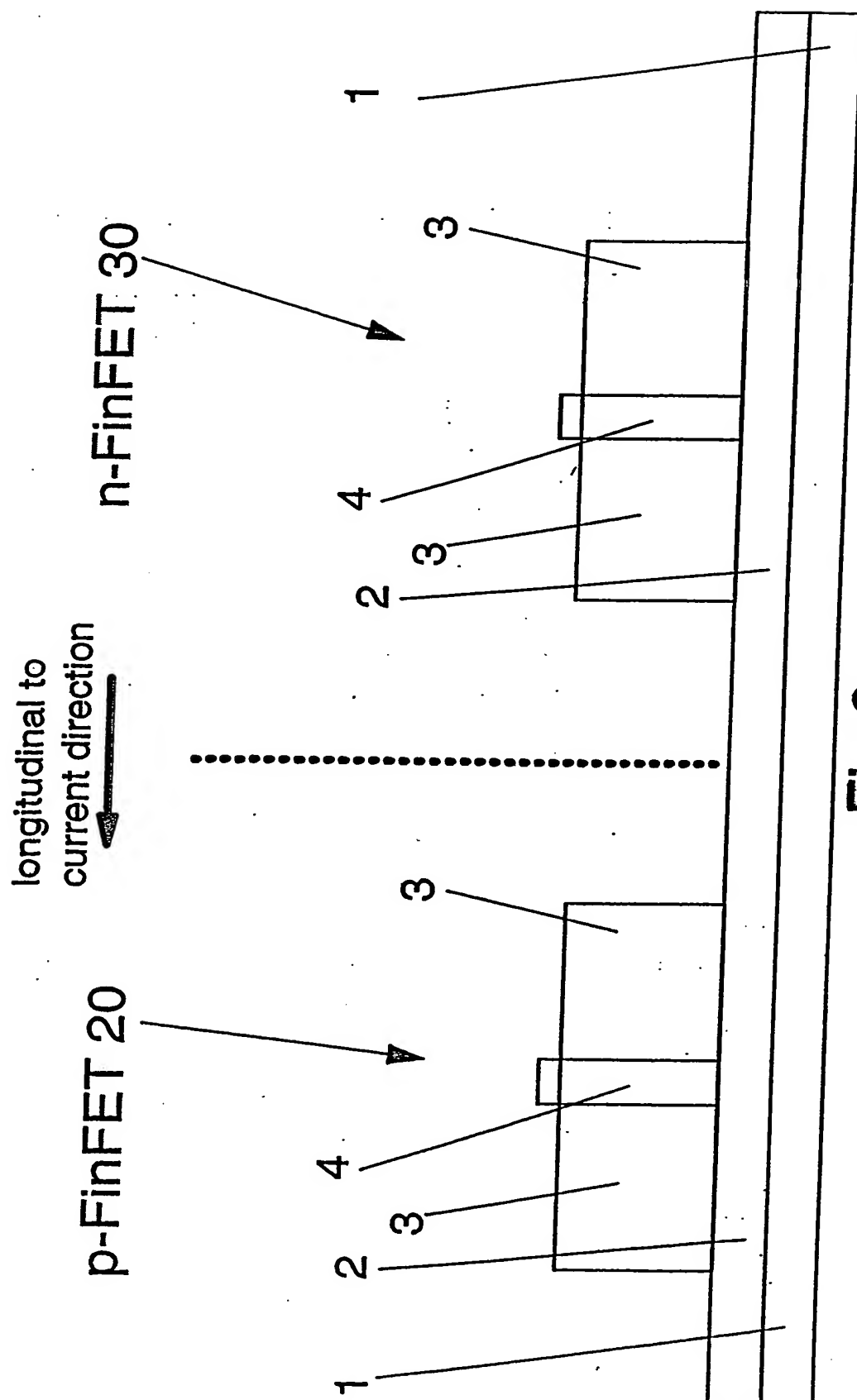


Fig 3
Prior Art

Deposit Thin SiO₂ Liner

Perp. view

p-FinFET longitudinal to
current direction n-FinFET

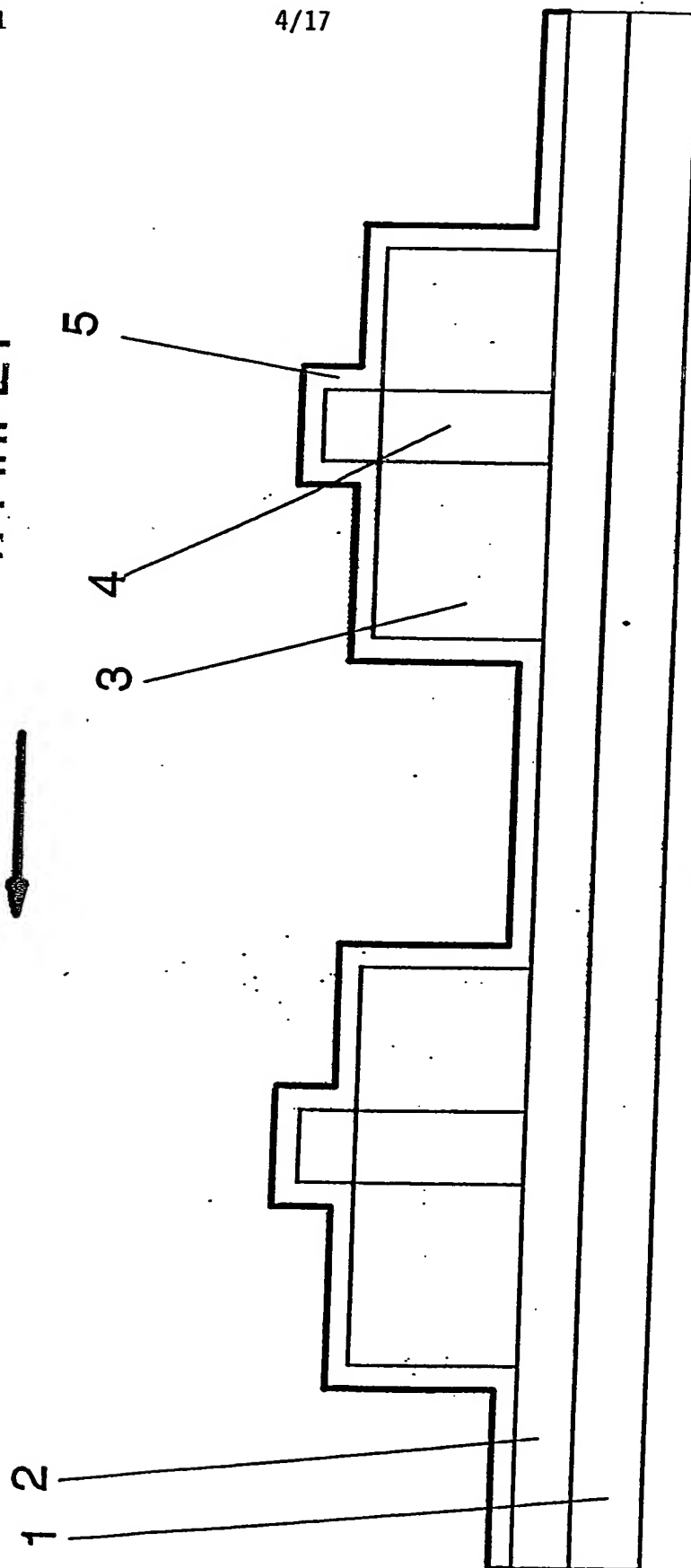


Fig 4

Deposit Thin SiO₂ Liner

Parallel view

p-FinFET

n-FinFET

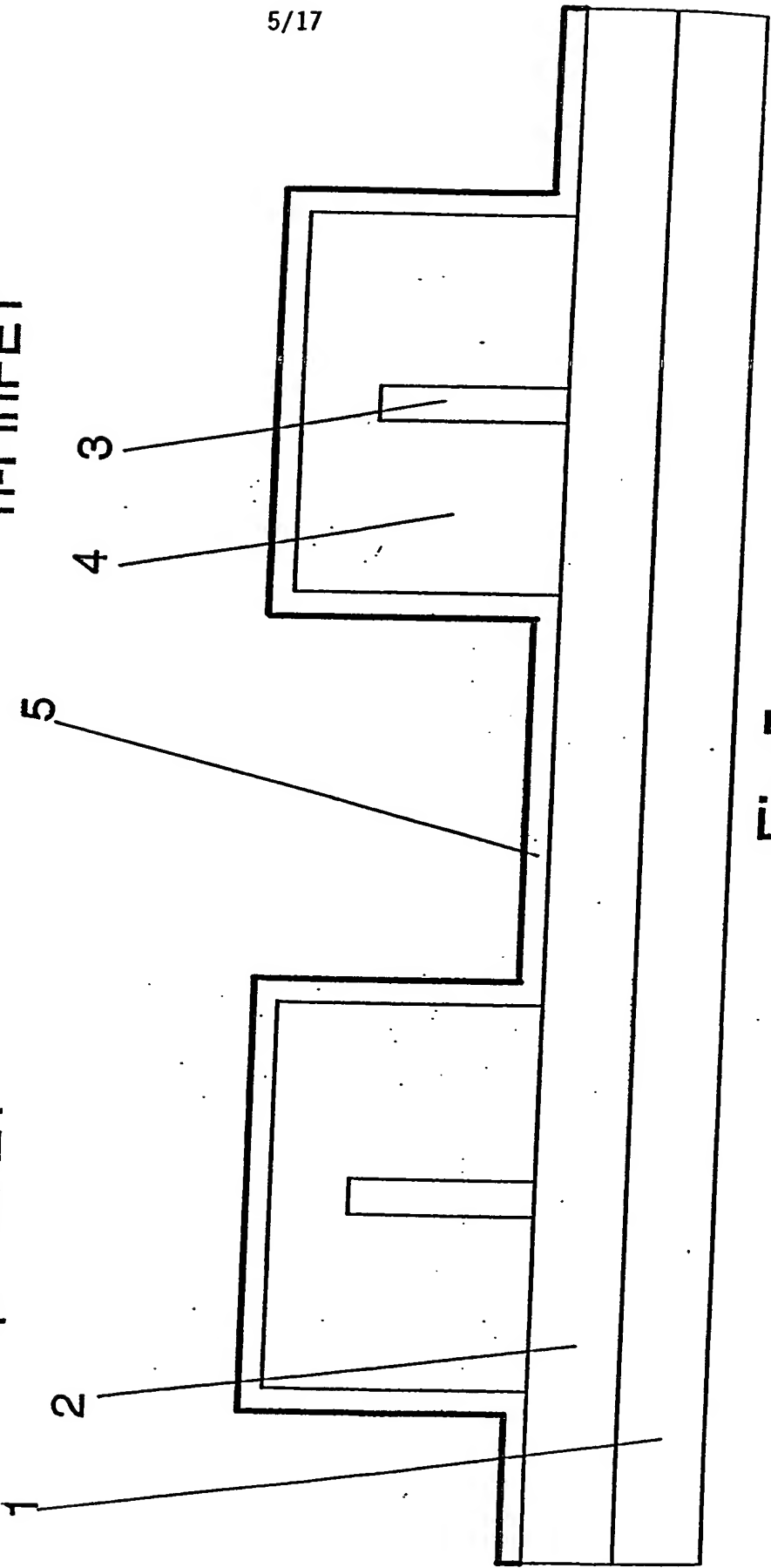


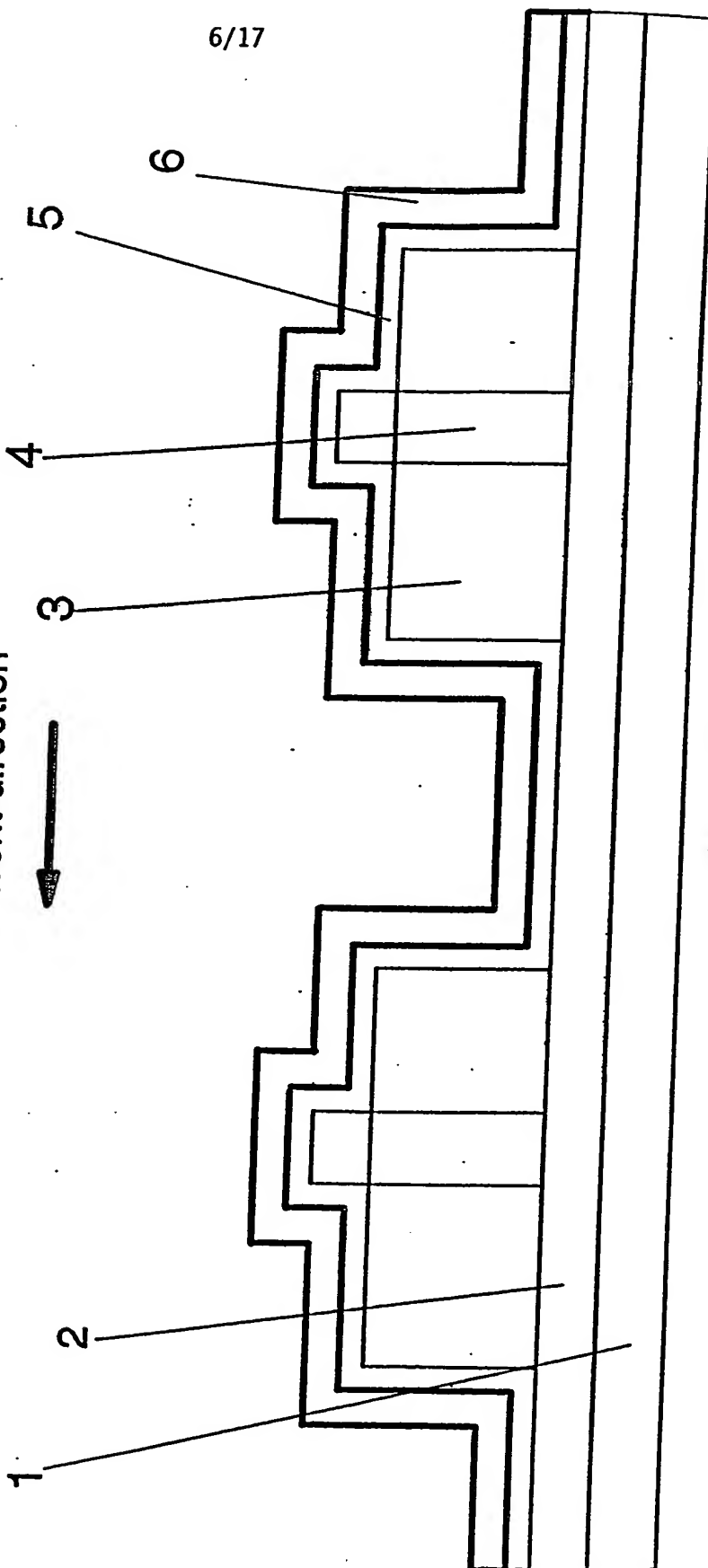
Fig 5

Deposit Compressive Film

Perp. view p-FinFET

n-FinFET

longitudinal to
current direction



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Fig 6

FIS920020105US1

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Deposit Compressive Film

Parallel view

p-FinFET

n-FinFET

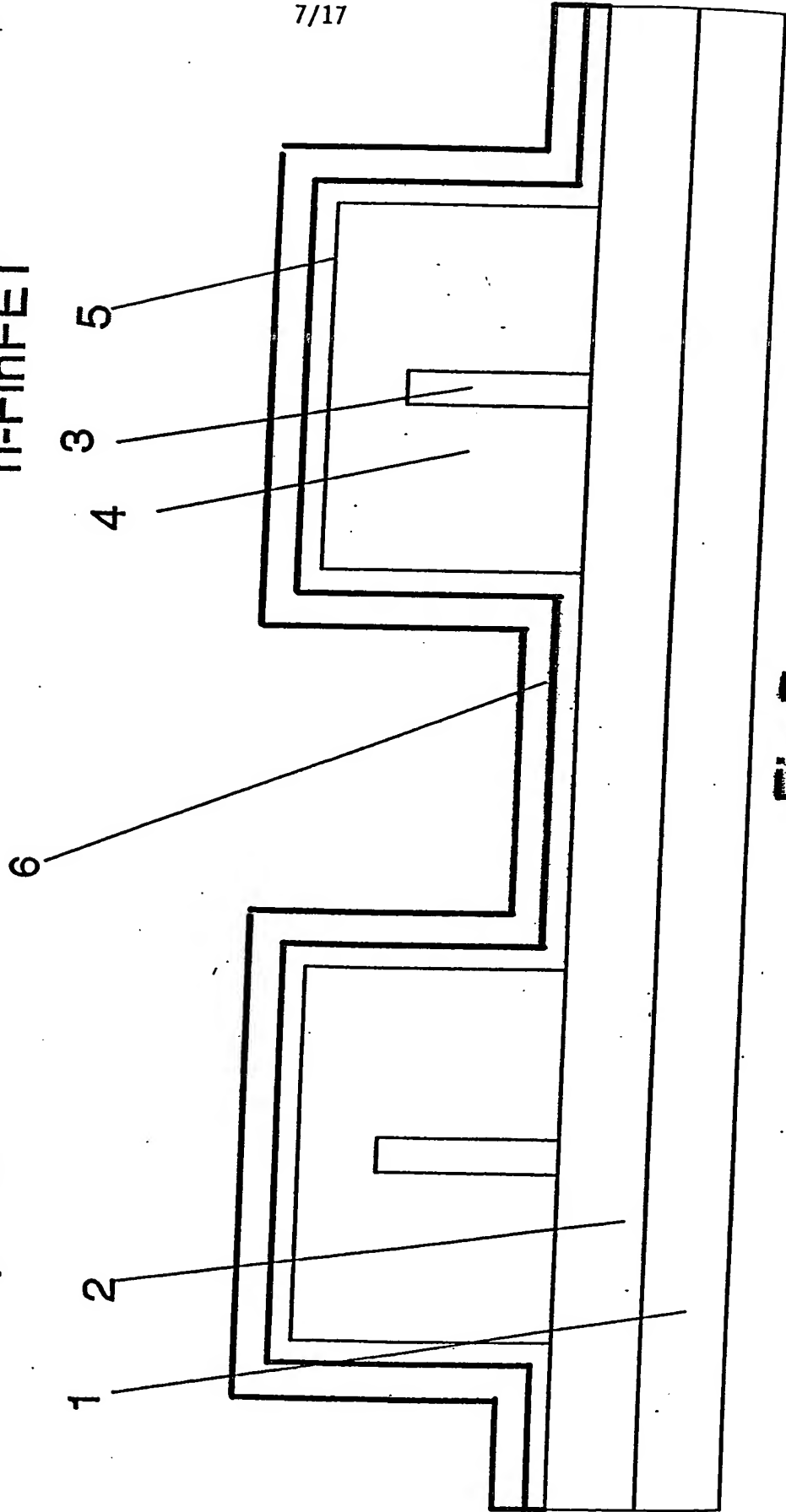


Fig 7

Perp. view Apply Block Mask

p-FinFET n-FinFET

longitudinal to
current direction
↓

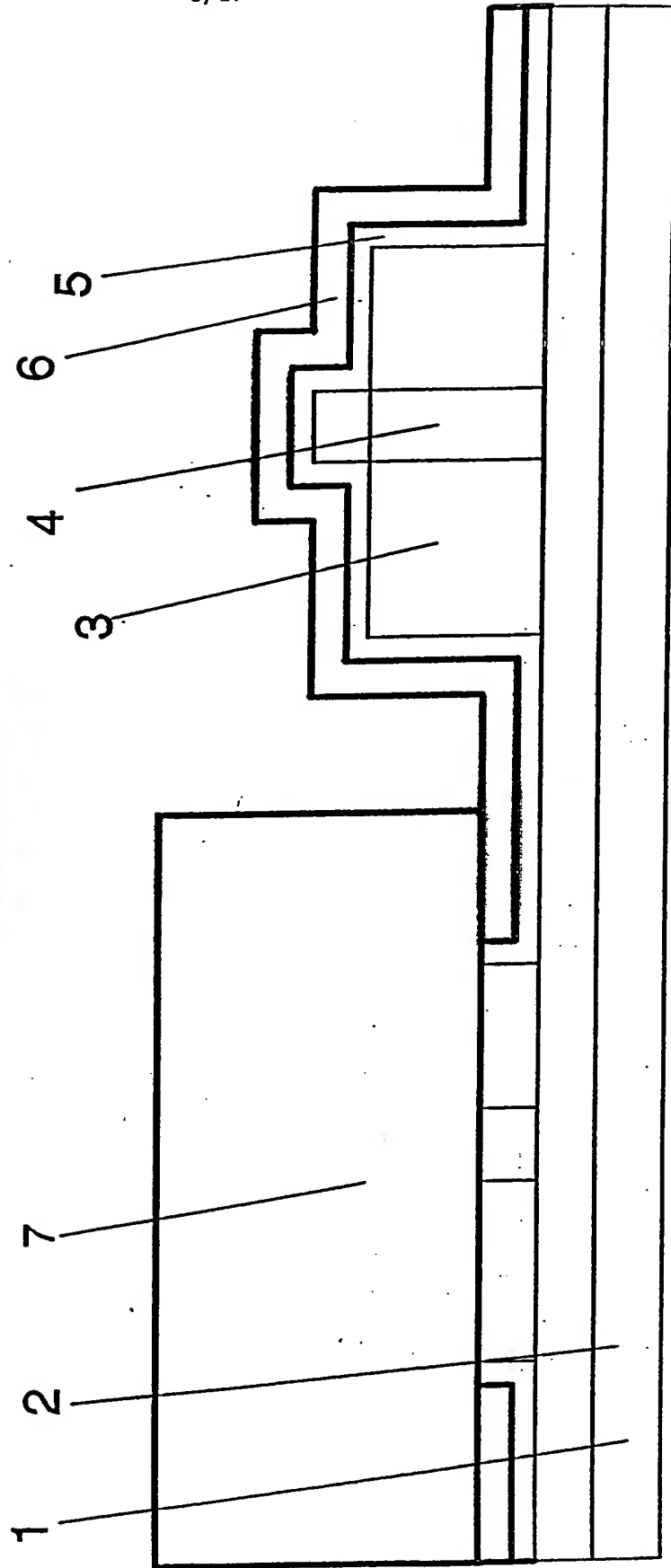


Fig 8

Apply Block Mask

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Parallel view

p-FinFET

n-FinFET

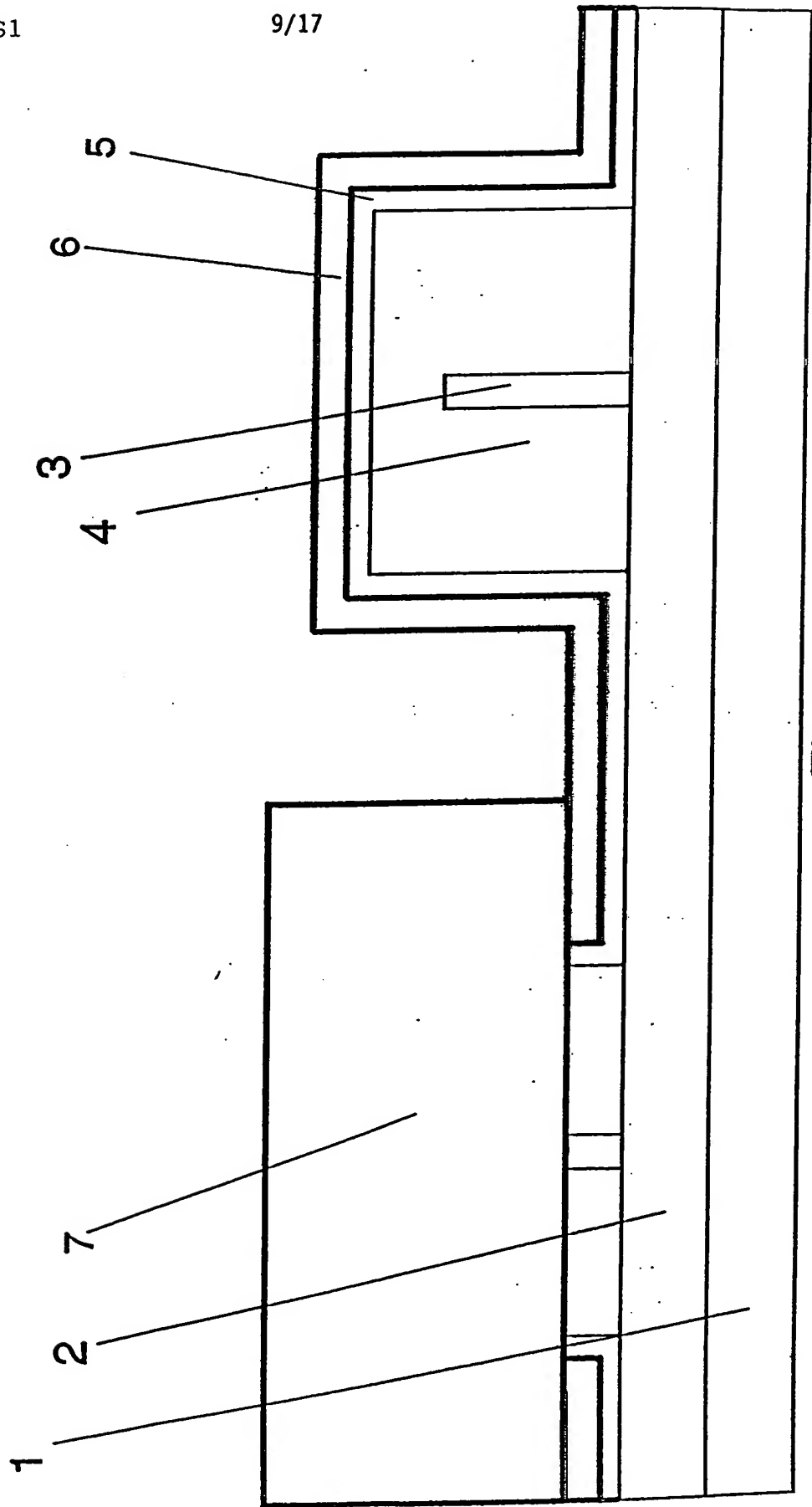


Fig 9

Remove Compressive Film From n-FinFET Stop on Liner

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Perp. view

p-FinFET

n-FinFET

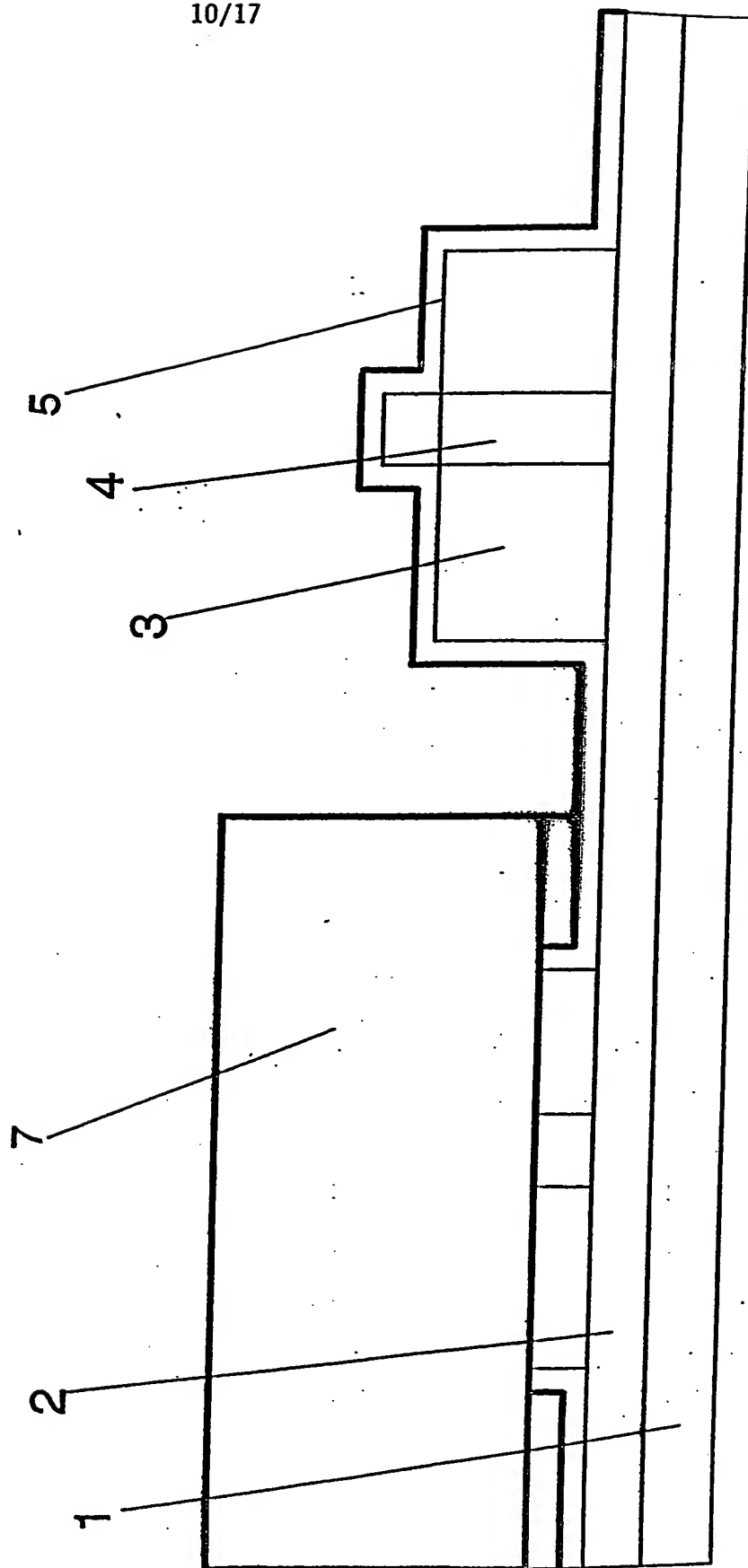
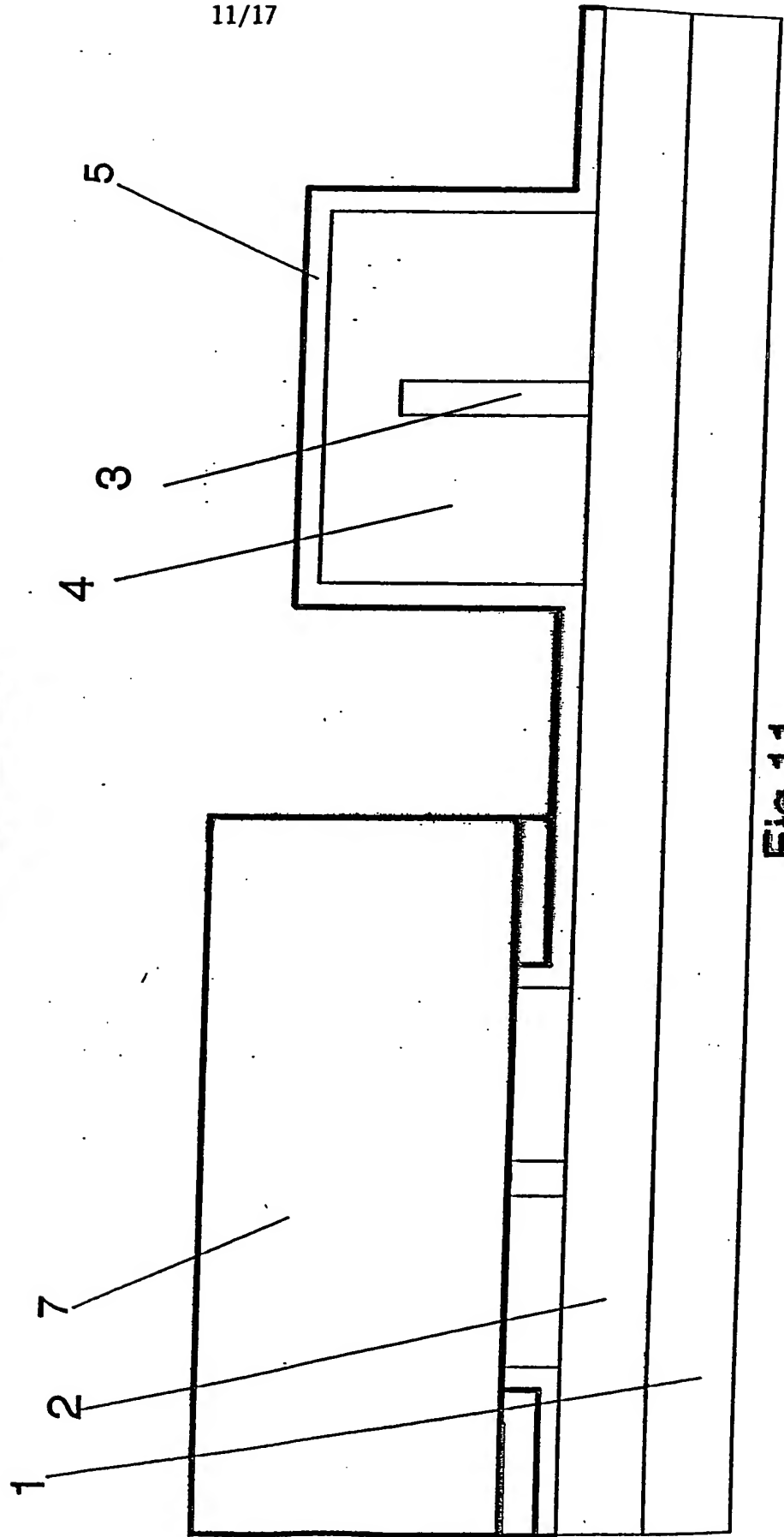


Fig 10

Parallel view

p-FinFET

n-FinFET



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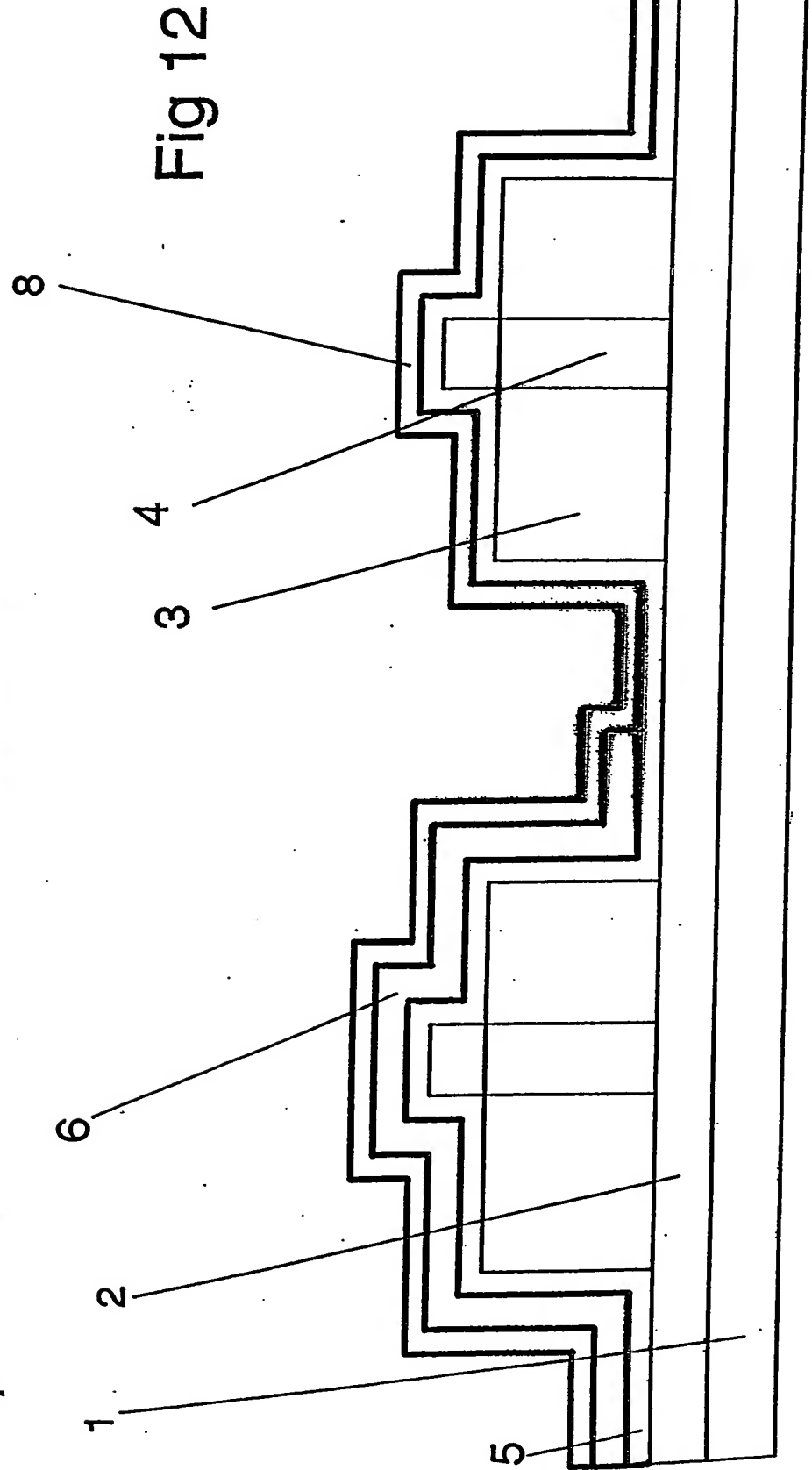
Remove Block Mask, Optional Liner 1 Removal, Deposit 2nd thin liner

Perp. view

longitudinal to
current direction
→

n-FinFET

p-FinFET



Remove Block Mask, Optional Liner 1 Removal, Deposit 2nd Thin Liner

Parallel view

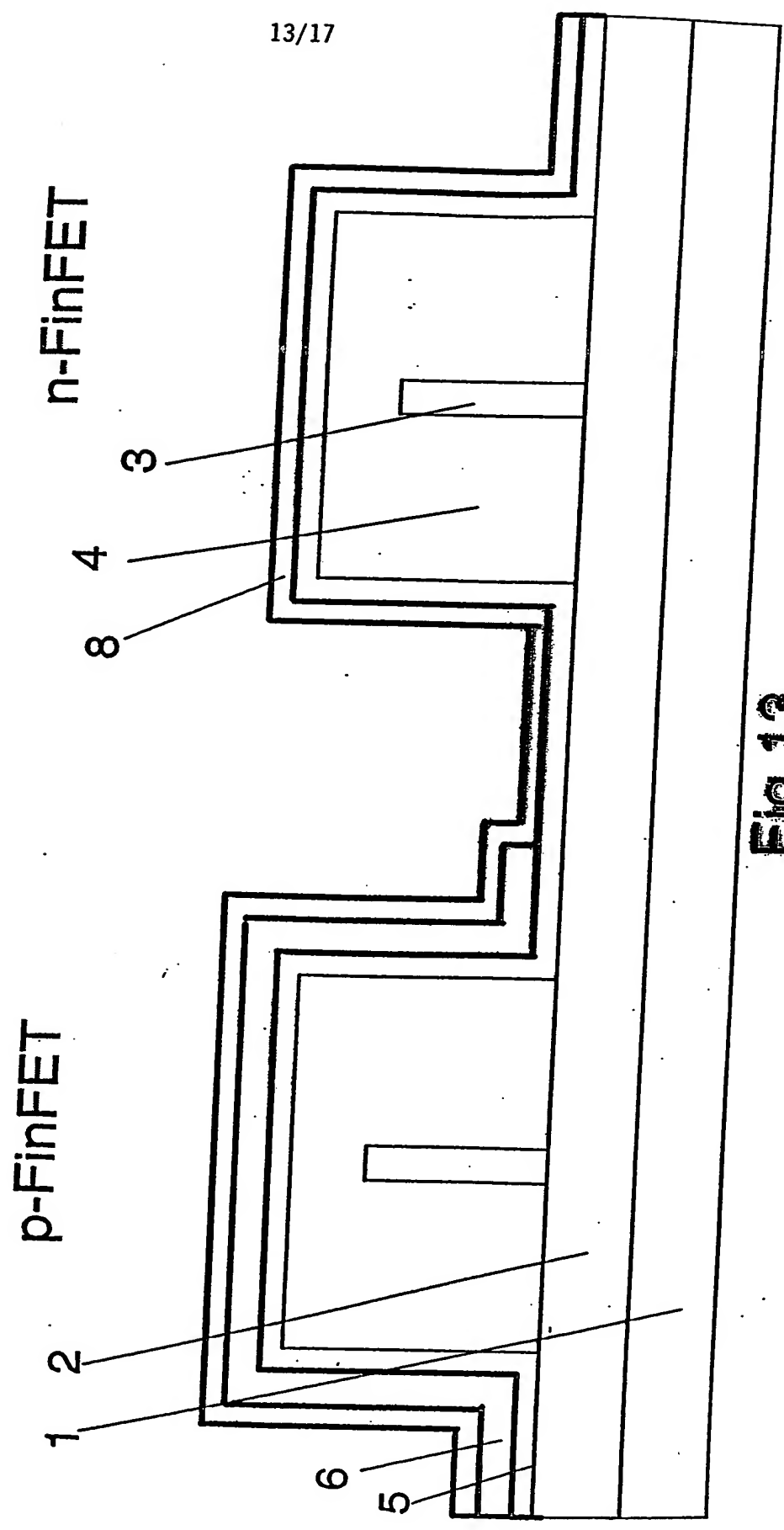
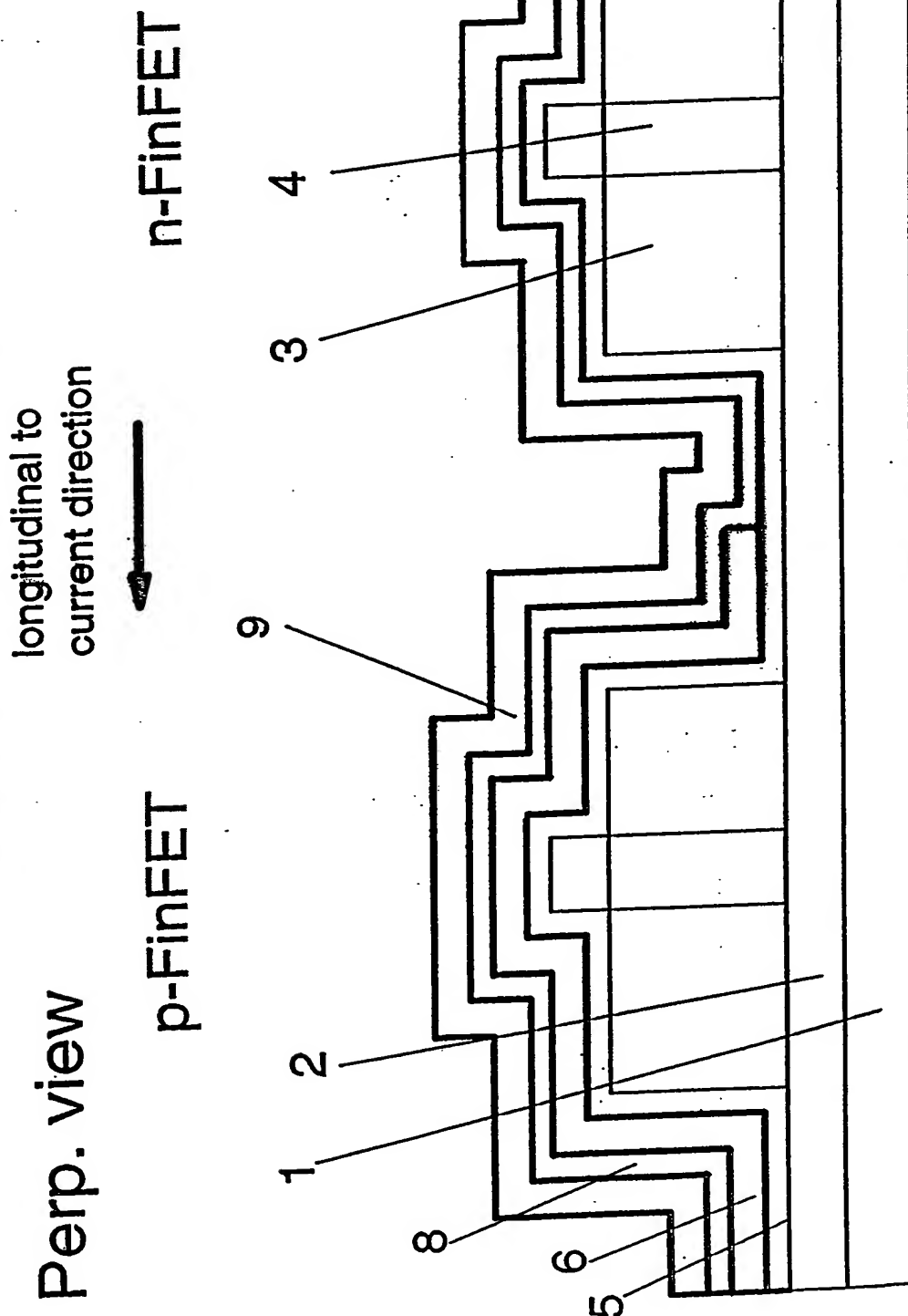


Fig 13

Deposit Tensile Film



Deposit Tensile Film

Parallel view

p-FinFET

n-FinFET

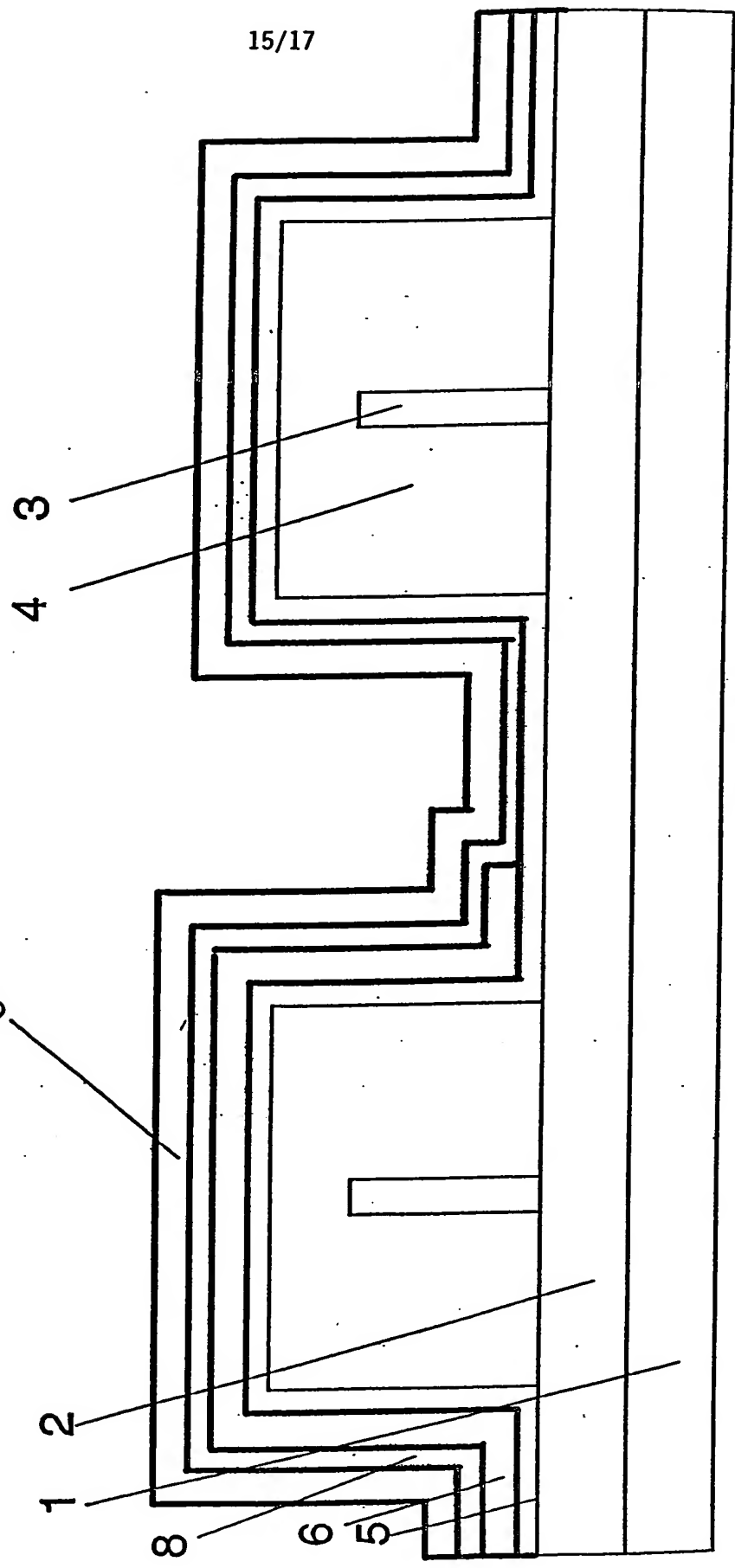


Fig 15

Apply Block Mask and Remove Tensile Film From p-FinFET

Perp. view

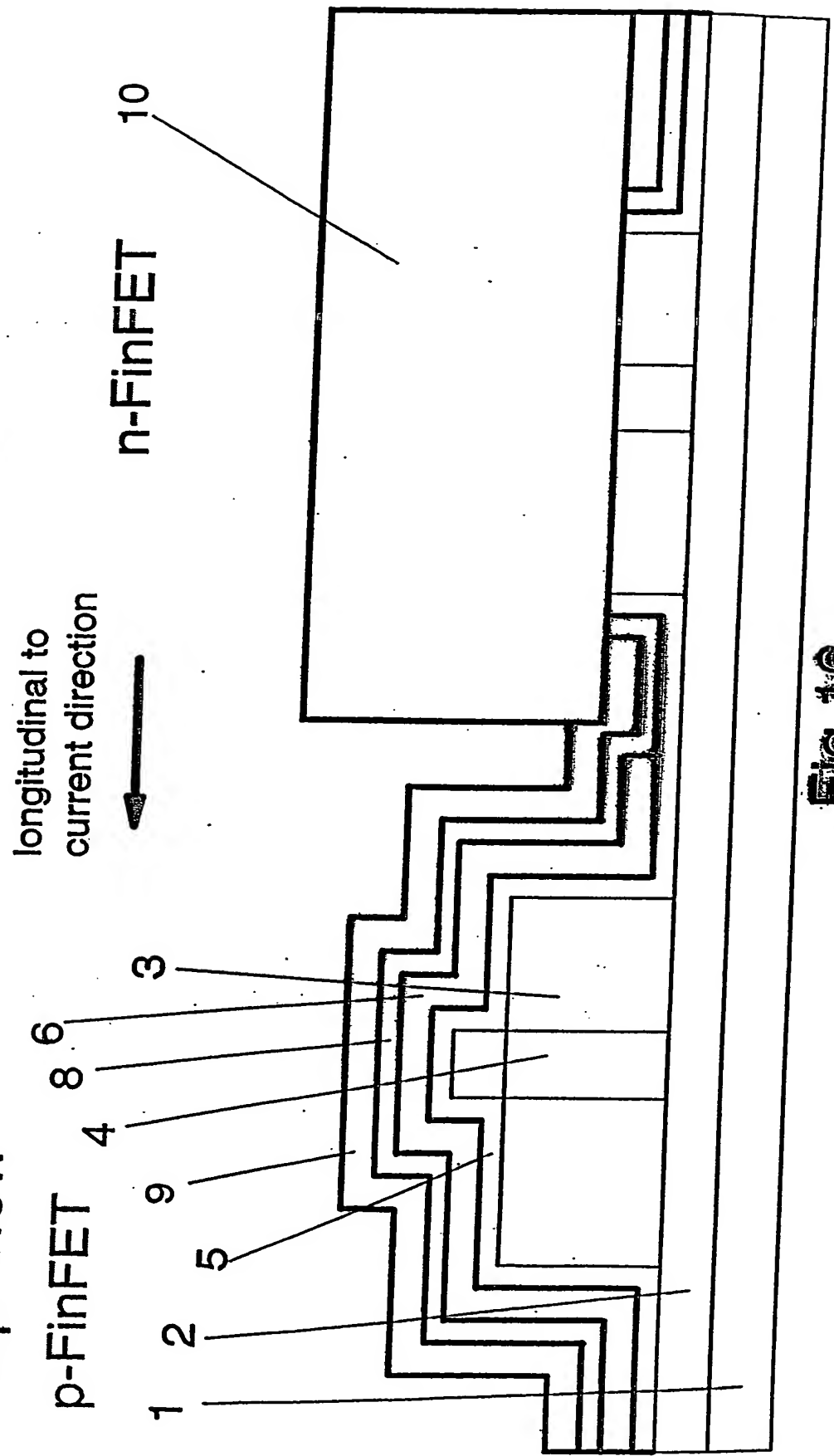


Fig 16

**Apply Block Mask and Remove Tensile Film From
p-FinFET, Remove Block Mask
Parallel view Final Device Structure**

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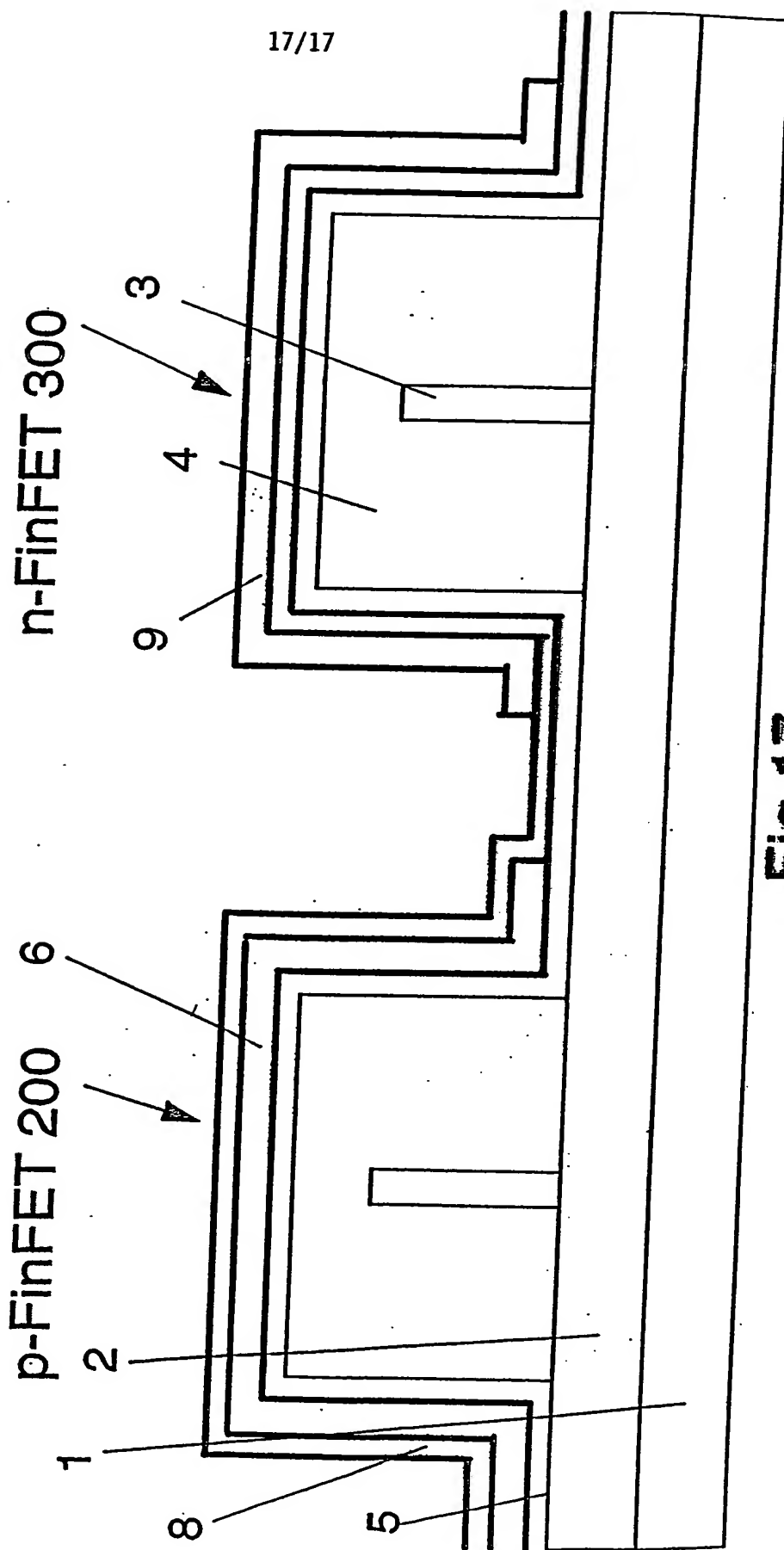


Fig 17

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